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## ABSTRACT OF THE DISCLOSURE

An output impedance bias compensation system adjusting output impedance of at least one output including a reference impedance generator, an impedance matching controller, at least one output impedance generator, and a programmable bias controller. The reference impedance generator develops a reference impedance based reference value. The impedance matching controller continually adjusts an input of the reference impedance to match the reference value within generator predetermined tolerance. Each output impedance generator is coupled to a corresponding output and is controlled by an output impedance control input. The programmable bias controller combines a bias amount with the value of the input of the reference impedance generator to provide the output impedance control input. The bias controller is programmable to provide a bias amount to compensate for any variations between the reference impedance generator and each output impedance generator.